



128Kx64 5V FLASH MODULE PRELIMINARY*

FEATURES

- Access Times of 60, 70, 90, 120, 150ns
- Packaging
 - 116 lead, 40mm square, Hermetic CQFP (Package 504)
- Sector Architecture
 - 8 equal size sectors of 16KBytes each
 - Any combination of sectors can be concurrently erased. Also supports full chip erase
- 100,000 Erase/Program Cycles Minimum (0°C to 70°C)
- Data Retention, 10 Year Minimum at 125°C
- Organized as 128Kx64, user configurable as 256Kx32, 512Kx16 or 1Mx8.
- Commercial, Industrial and Military Temperature Ranges
- 5 Volt Programming; 5V (±10%) Supply
- Low Power CMOS, 8mA Standby Typical
- Hardware and Software Write Protection
- TTL Compatible Inputs and Outputs
- Built-in Decoupling Caps and Multiple Ground Pins for Low Noise Operation
- Weight
WF128K64-XG4WX5 - 20 grams typical

** This data sheet describes a product under development and is subject to change without notice.*

Note: Programming information available upon request.

FIG. 1 PIN CONFIGURATION FOR WF128K64-XG4WX5

