

IS27C256

32,768 x 8 CMOS EPROM

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FEATURES

- Read access time: 90 ns
- JEDEC-approved pinout
- High-speed write programming
 - Typically less than four seconds
- 5V \pm 10% power supply tolerance
- Both CMOS and TTL compatible input and output
- Two line control functions
- Industrial and commercial temperature ranges available

DESCRIPTION

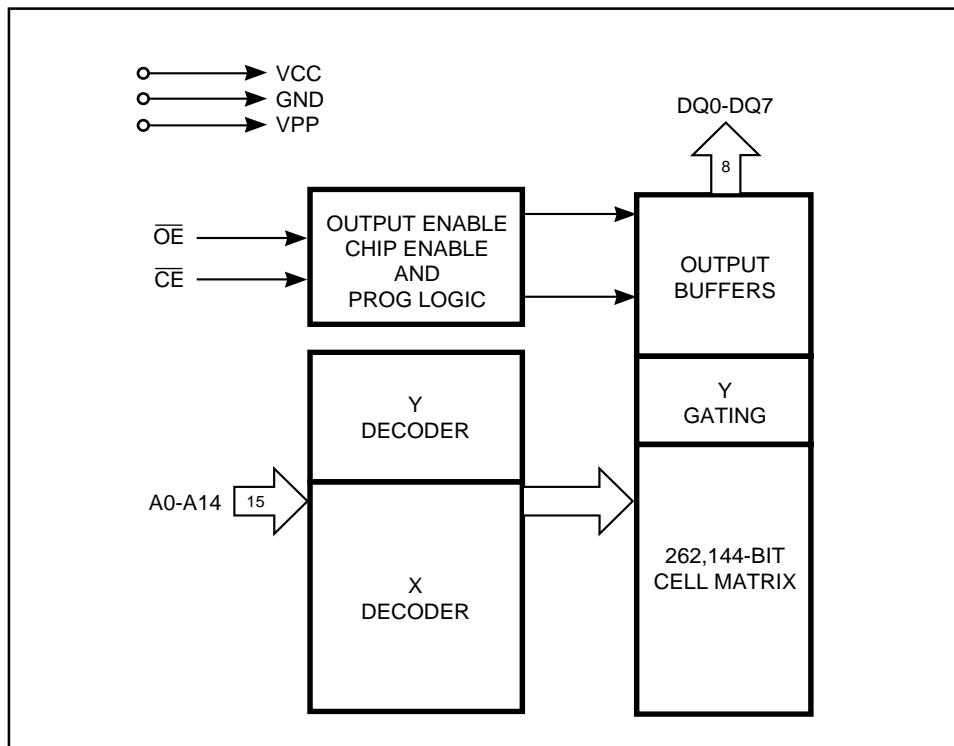
The *ISSI* IS27C256 is a 256 K-bit (32K-word by 8-bit) CMOS Programmable Read-Only Memory. It requires only a single 5V power supply in normal read mode operation. Any byte can be accessed in less than 90 ns. The IS27C256 offers separate Output Enable (\overline{OE}) and Chip Enable (\overline{CE}) controls, thus eliminating bus contention in a multiple bus microprocessor system.

All signals are TTL levels, including programming signals. Bit locations may be programmed singly, in blocks, or at random.

The IS27C256 uses *ISSI's* write programming algorithm. Programming time is typically only 100 μ s per byte.

This product is available in One-Time Programmable (OTP) PDIP and PLCC packages over commercial and industrial temperature ranges.

FUNCTIONAL BLOCK DIAGRAM



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