



A25L032

32Mbit Low Voltage, Serial Flash Memory

With 100MHz Uniform 4KB Sectors

Preliminary

FEATURES

- Family of Serial Flash Memories
 - A25L032: 32M-bit /4M-byte
- Flexible Sector Architecture with 4KB sectors
 - Sector Erase (4K-bytes) in 0.4s (typical)
 - Block Erase (64K-bytes) in 1s (typical)
- Page Program (up to 256 Bytes) in 3ms (typical)
- 2.7 to 3.6V Single Supply Voltage
- Dual input / output instructions resulting in an equivalent clock frequency of 200MHz:
 - Dual Output Fast Read Instruction
 - Dual Input and Output Fast Read Instruction
 - Dual Input Fast Program Instruction
- SPI Bus Compatible Serial Interface
- 100MHz Clock Rate (maximum)
- Deep Power-down Mode 1 μ A (typical)
- Additional 64-byte user-lockable, one-time programmable (OTP) area
- 32 Mbit Flash memory
 - Uniform 4-Kbyte sectors
 - Uniform 64-Kbyte blocks
- Electronic Signatures
 - JEDEC Standard Two-Byte Signature A25L032: (3016h)
 - RES Instruction, One-Byte, Signature, for backward compatibility A25L032 (15h)
- Package options
 - 8-pin SOP (209mil), 16-pin SOP (300mil), 8-pin DIP (300mil)
 - All Pb-free (Lead-free) products are RoHS compliant

GENERAL DESCRIPTION

The A25L032 are 32M bit Serial Flash Memory, with advanced write protection mechanisms, accessed by a high speed SPI-compatible bus.

The memory can be programmed 1 to 256 bytes at a time, using the Page Program instruction.

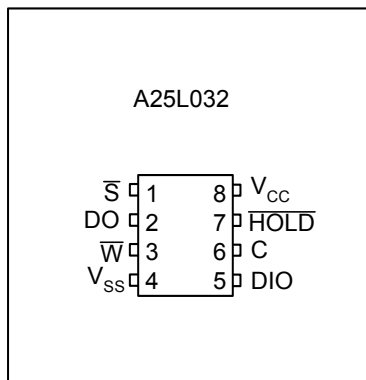
The memory is organized as 64 blocks, each containing 16

sectors. Each sector is composed of 16 pages. Each page is 256 bytes wide. Thus, the whole memory can be viewed as consisting of 16,384 pages, or 4,194,304 bytes.

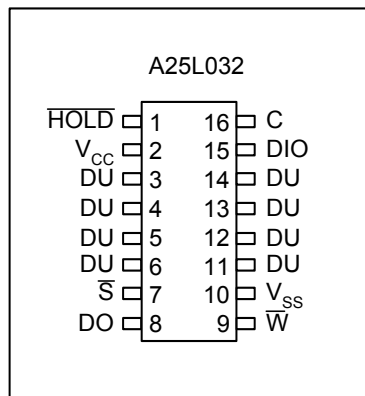
The whole memory can be erased using the Chip Erase instruction, a block at a time, using Block Erase instruction, or a sector at a time, using the Sector Erase instruction.

Pin Configurations

■ SOP8 Connections



■ SOP16 Connections



Note:
DU = Do not Use

■ DIP8 Connections

